



11-10-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:
TANGE NAN BARBOUR ET AL.

Serial No. 10/604887

Filed: 8-25-03

For: **METHOD AND SYSTEM
FOR DETERMINING MINIMUM
POST PRODUCTION TEST TIME
REQUIRED ON AN INTEGRATED
ON AN INTEGRATED CIRCUIT
DEVICE TO ACHIEVE OPTIMUM
RELIABILITY**

§ Attorney Docket No.: **BUR920030044US1**

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Substitute form 1449/PTO

				Complete if Known	
				Application Number	10/604887
				Filing Date	8-25-03
				First Named Inventor	Tange Nan Barbour et al.
				Group Art Unit	Not yet assigned 2858
				Examiner Name	Not yet assigned
Sheet	1	of	1	Attorney Docket Number	BUR920030044US1
U.S. PATENT DOCUMENTS					
Examiner Initials	IDS Doc #	U.S. Patent Document Number	Name of Patentee or Applicant		Publication Date MM-DD-YYYY
	AA	US 2003/0120445 A1	Tange N. Barbour et al.		06/26/2003
FOREIGN PATENT DOCUMENTS					
Examiner Initials	IDS Doc #	Office	Foreign Patent Document No.	Name of Patentee or Applicant	Date of Publication
OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	IDS Doc #	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			English Translation
	AB	THOMAS S. BARNETT, ADIT D. SINGH AND VICTOR P. NELSON; "Estimating Burn-In Fall-Out for Redundant Memory", Department of Electrical and Computer Engineering, Auburn University, Auburn, AL 36849, 8 pages			Yes
Examiner Signature				Date Considered	

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